

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicants: Dougherty et al.

Examiner: P. Dinh

Serial No: 10/780,140

Group Art Unit: 2825

Filed: February 17, 2004

Docket: YOR920030437US1  
(8728-653)

For: METHOD FOR OPTIMIZATION OF LOGIC CIRCUITS FOR  
ROUTABILTY IMROVEMENT

Mail Stop Amendment  
Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT UNDER 37 C.F.R. § 1.111**

Sir:

This is a response to the Office Action mailed August 6, 2007.